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PRODUCT SPECIFICATION

Model No	E042QJ-W-H16-03
Product Name	0.42 OLED Module
Version	V1.0
Date	2025-08-08

- ☐ Preliminary Specification
☒ Final Specification

PREPARED BY	CHECKED BY	APPROVED BY

Revision History

Version	Date	Page	Revise record	Remarks
1.0	2025.08.08	-	First issued	-

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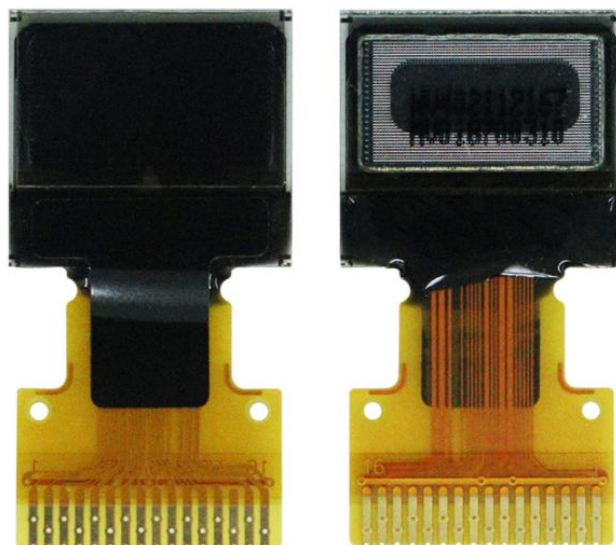
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1. General Parameter

NO.	Features	Details	Unit
1	Display Size(Diagonal)	0.42"	Inch
2	LCD type	OLED	-
3	View Direction	ALL	-
4	Drive Duty	1/64	-
5	Resolution	72 x 40	Pixels
6	Module size	12.0(W) x 11.0(H) x 1.28(T)	mm
7	Active Area	9.196(W) x 5.18(H)	mm
8	Pixel Pitch	0.128(W) x 0.13(H)	mm
9	Pixel Size	0.108(W) x 0.11(H)	mm
10	Interface	I ² C, 4-Wire SPI	-
11	Display Colors	White	-
12	Driver IC	SSD1315	-
13	IC package type	COG	-
14	Operating Temperature	-20℃~70℃	℃
15	Storage Temperature	-30℃~80℃	℃
16	Weight	-	g

Note 1 : Please refer to the mechanical drawing.

2. Physical drawing



3. Mechanical Dimension

[illegible]

4. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit
Logic Supply Voltage	VDD	-0.3	+4	V
Charge Pump Regulator Supply Voltage	VBAT	-0.3	+6	V
Operating Voltage	VCC	0	16	V
Operating temperature	Top	-20	70	°C
Storage temperature	Tst	-30	80	°C

Note 1 : All the above voltages are on the basis of “VSS = 0V” .

Note 2 : When this module is used beyond the above absolute maximum ratings, permanent breakage of the module may occur. Also, for normal operations, it is desirable to use this module under the conditions according “Optics & Electrical Characteristics” . If this module is used beyond these conditions, malfunctioning of the module can occur and the reliability of the module may deteriorate.

Note 3 : The defined temperature ranges do not include the polarizer. The maximum withstood temperature of the polarizer should be 80°C.

Note 4 : 50% Checkerboard.End of lifetime is specified as 50% of initial brightness reached. The average operating lifetime at room temperature is estimated by the accelerated operation at high temperature conditions.

5. DC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
Logic Supply Voltage	VDD	1.65	-	3.5	V
Charge Pump Regulator Supply Voltage	VBAT	3.0	-	4.5	V
Operating Voltage	VCC	7.5	-	16	V
Input high voltage	V _{IN}	0.8*VDD	-	-	V
Input low voltage	V _{IL}	-	-	0.2*VDD	V
Output high voltage	V _{OH}	0.9*VDD	-	-	V
Output low voltage	V _{OL}	-	-	0.1*VDD	V
VCC Supply Current	ICC	-	625	1000	uA
VDD Supply Current	IDD	-	160	220	uA

Note 1 : Brightness (Lbr) and Supply Voltage for Display (VCC) are subject to the change of the panel characteristics and the customer’ s request.

Note 2 : The above refers to the DC characteristics of the LCD screen.

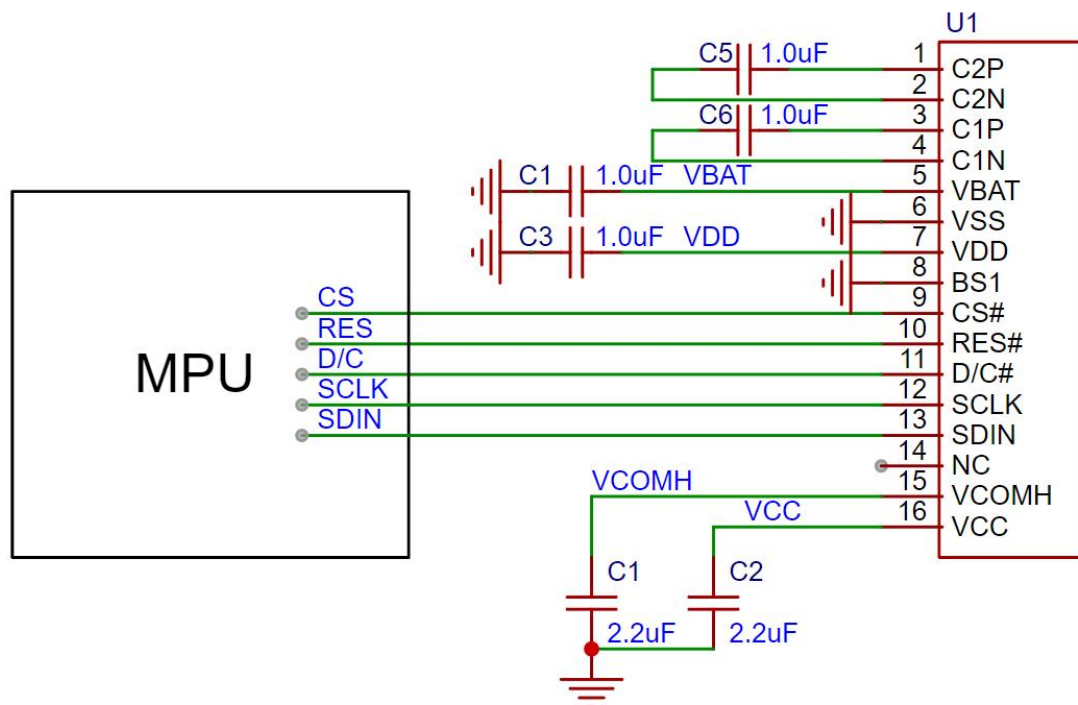
6. Pins Definitions.

Pin.No	Symbol	Description			
1	C2P	for charge pump capacitor; Connect to each other with a capacitor.			
2	C2N				
3	C1P	for charge pump capacitor; Connect to each other with a capacitor.			
4	C1N				
5	VBAT	Power supply for charge pump regulator circuit.			
		Status	VBAT	VDD	VCC
		Enable charge pump	Connect to external VBAT source	Connect to external VDD source	A capacitor should be connected between this pin and VSS
		Disable charge pump	Keep float	Connect to external VDD source	Connect to external VCC source
6	VSS	Ground pin. It must be connected to external ground.			
7	VDD	Power supply pin for core logic operation.			
8	BS1	When the pin is pulled HIGH, interface selection I ² C. When the pin is pulled LOW, interface selection 4-Wire SPI.			
9	CS#	This pin is the chip select input connecting to the MCU. The chip is enabled for MCU communication only when CS# is pulled LOW (active LOW).			
10	RES#	This pin is reset signal input. When the pin is pulled LOW, initialization of the chip is executed. Keep this pin HIGH (i.e. connect to VDD) during normal operation.			
11	D/C	This is the Data/Command control pad that determines whether the data bits are data or a command. In I2C interface, this pad serves as SA0 to distinguish the different address of OLED driver.			
12	SCLK	Serial Clock Input Pad			
13	SDIN	Serial Data Input Pad.			
14	NC	No Connect.			
15	VCOMH	COM signal deselected voltage level. A capacitor should be connected between this pin and VSS			
16	VCC	Power supply for panel driving voltage. This is also the most positive power voltage supply pin. When charge pump is enabled, a capacitor should be connected between this pin and VSS.			

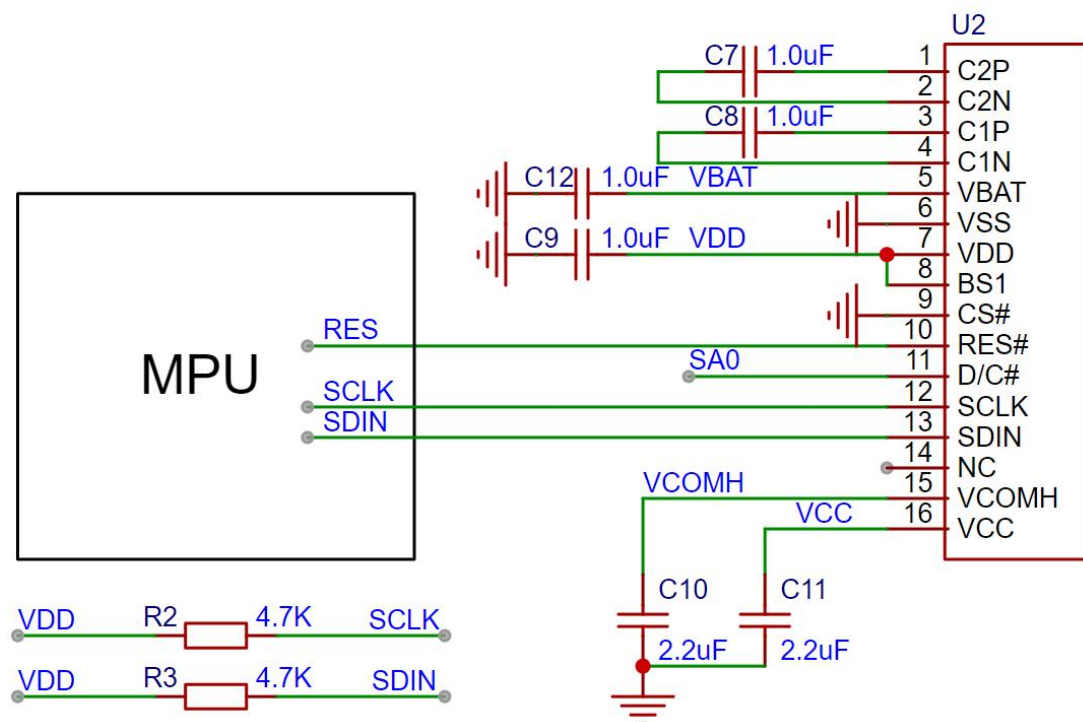
Note 1 : See the IC Specification manual for details.

7. Schematic Diagram.

4线SPI 参考原理图



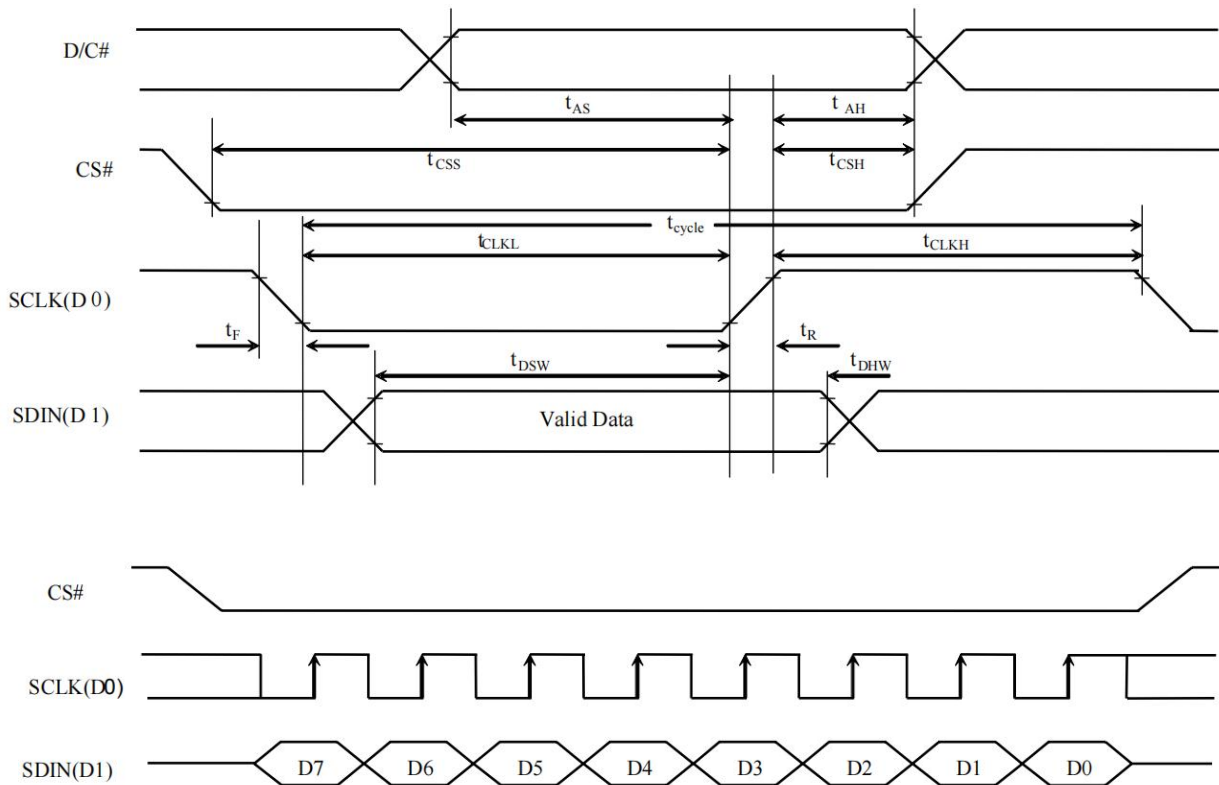
I2C 参考原理图



R1: about 310kΩ (ISEG=300uA) , R1 = (Voltage at IREF - VSS)/IREF

8. Timing Characteristics.

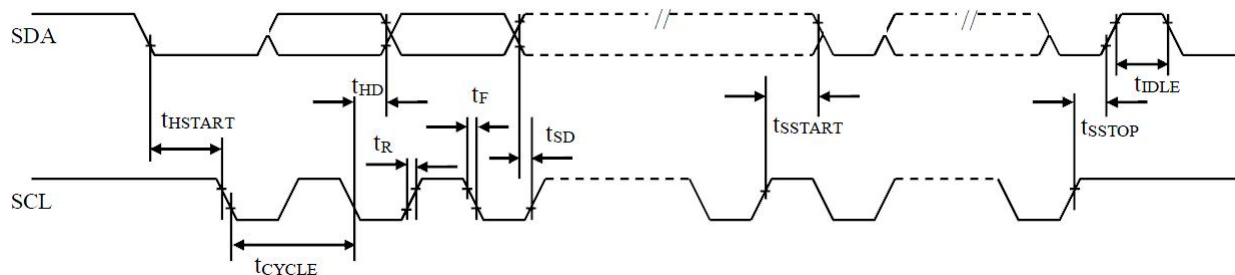
Serial interface characteristics (4 Wire SPI)



(VDD - VSS = 1.65V~3.5V, TA = 25°C)

Symbol	Parameter	Min	Typ	Max	Unit
t_{cycle}	Clock Cycle Time	100	-	-	ns
t_{AS}	Address Setup Time	15	-	-	ns
t_{AH}	Address Hold Time	15	-	-	ns
t_{CSS}	Chip Select Setup Time	20	-	-	ns
t_{CSH}	Chip Select Hold Time	20	-	-	ns
t_{DSW}	Write Data Setup Time	15	-	-	ns
t_{DHW}	Write Data Hold Time	25	-	-	ns
t_{CLKL}	Clock Low Time	30	-	-	ns
t_{CLKH}	Clock High Time	30	-	-	ns
t_R	Rise Time	-	-	40	ns
t_F	Fall Time	-	-	40	ns

Serial interface characteristics (I²C)



Symbol	Parameter	Min	Typ	Max	Unit
t_{cycle}	Clock Cycle Time	2.5	-	-	us
t_{HSTART}	Start condition Hold Time	0.6	-	-	us
t_{HD}	Data Hold Time (for "SDA _{OUT} " pin)	0	-	-	ns
	Data Hold Time (for "SDA _{IN} " pin)	300	-	-	ns
t_{SD}	Data Setup Time	100	-	-	ns
$t_{SSSTART}$	Start condition Setup Time (Only relevant for a repeated Start condition)	0.6	-	-	us
t_{SSSTOP}	Stop condition Setup Time	0.6	-	-	us
t_{R}	Rise Time for data and clock pin	-	-	300	ns
t_{F}	Fall Time for data and clock pin	-	-	300	ns
t_{IDLE}	Idle Time before a new transmission can start	1.3	-	-	us

9. Optical Characteristics.

项目	标志符号	测试条件	最小值	典型值	最大值	单位
正常模式亮度 (带偏光片)	L _{br}	所有像素点亮 ⁽¹⁾	320	400	-	cd/m ²
正常模式电流消耗	Pt	所有像素点亮 ⁽¹⁾	-	88.8	115.44	mW
休眠模式下 VCC 电流消耗	ICC,SLEEP	VDD = 1.65V~3.3V, VCC = 7V~15V 显示关闭, 没有面板连接。	-	-	20	uA
休眠模式下 VDD 电流消耗	IDD,SLEEP	VDD = 1.65V~3.3V, VCC = 7V~15V 显示关闭, 没有面板连接。	-	-	20	uA
休眠模式下 VBAT 电流消耗	IBAT,SLEEP	VDD = 1.65V~3.3V, VCC = 7V~15V 显示关闭, 没有面板连接。	-	-	20	uA
C.I.E(白光)	(X)	x,y(CIE1931)	0.22	0.25	0.28	-
	(Y)		0.24	0.27	0.30	-
暗室对比度	CR	-	10000:1	-	-	-
响应时间	-	-	-	10	-	μs
视角	-	-	> 160	-	-	Degree

注(1): 正常模式测试条件如下:

-驱动电压: VBAT:3.7V(VCC 由内部 DCDC 电路提供)

-对比度设置: 0xFF

-帧频设置: 105Hz

-占空比设置: 1/40

10. Reliability

10.1. MTBF

The LCD module shall be designed to meet a minimum MTBF value of 50000 hours with normal.

10.2. Test Condition

No	Item	Condition	Criteria
1	High Temperature Operating	70℃*24Hrs	1. No Defect Of Operational Function In Room Temperature Are Allowable. 2. IDD of LCM in Pre-and Post-Test Should Follow Specification.
2	Low Temperature Operating	-20℃*24Hrs	
3	High Humidity Storage	50℃*90%RH*24Hrs	
4	High Temperature Storage	80℃*24Hrs	
5	Low Temperature Storage	-30℃*24Hrs	
6	Thermal Cycling Test Storage	-	
7	Packing vibration	-	
8	Electrical Static Discharge	-	
		-	
9	Drop Test (Packaged)	-	

Note 1 : The test samples should be applied to only one test item.

Note 2 : Sample size for each test item is 2pcs.

Note 3 : No defective function allowable.

11. Precautions

11.1. Storage Conditions

- 11.1.1. Store the panel or module in a dark place where the temperature is $23\pm5^{\circ}\text{C}$ and the humidity is below $45\pm 20\%\text{RH}$.
- 11.1.2. Store in anti-static electricity container.
- 11.1.3. Store in clean environment, free from dust, active gas, and solvent.
- 11.1.4. Do not place the module near organics solvents or corrosive gases.
- 11.1.5. Do not crush, shake, or jolt the module.
- 11.1.6. Strong light exposure causes degradation of polarizer and color filter.

11.2. Handling Precautions

- 11.2.1. Avoid static electricity, which can damage the CMOS LSI.
- 11.2.2. The polarizing plate of the display is very fragile, please handle it very carefully.
- 11.2.3. Do not give external shock.
- 11.2.4. Do not apply excessive force on the surface.
- 11.2.5. Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.
- 11.2.6. Reverse and use within ratings in order to keep performance and prevent damage
- 11.2.7. Do not remove the panel or frame from the module.
- 11.2.8. Except for soldering the interface, do not make any alterations or modifications with a soldering iron; Ensure welding temperature at 320°C to 350°C , the welding time control within the 10 s, welding note don't stay too long in the same place to avoid scald FPC.

11.3. Limited Warranty

- 11.3.1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss. (我们的保证责任仅限于修理和/或更换。我们将不负任何相应的损失。)
- 11.3.2. If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used. (如果可能, 我们建议客户在 6 个月内用完所有模块。如果模块存放时间超过 12 个月, 我们建议在使用模块前重新检查。)
- 11.3.3. The warranty period is twelve months from the date of delivery. Buyer shall complete the assembly of all processes within twelve months of validity. During the warranty period, if the product quality problems, our company will be responsible for repair and replacement. All products must be stored and handled in accordance with regulations. Under warranty. When the goods do not comply with the above terms, we do not provide warranty services. (保修期为交货之日起十二个月。买方应在 12 个月内完成所有流程的组装。在保修期内, 如果产品出现质量问题, 我公司将负责维修和更换。所有产品必须按照规定储存和处理。在保修期内。当货物不符合上述条款时, 我们不提供保修服务。)