

深 圳 市 金 逸 晨 电 子 有 限 公 司

LCD MODULE

MODULE NO. :

0.49" I2C SERIES

Customer:

Approved By(核准) :

深圳市金逸晨电子有限公司

Approved By(核准) :

Checked By(审核) :

Prepared By(编写) :

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A

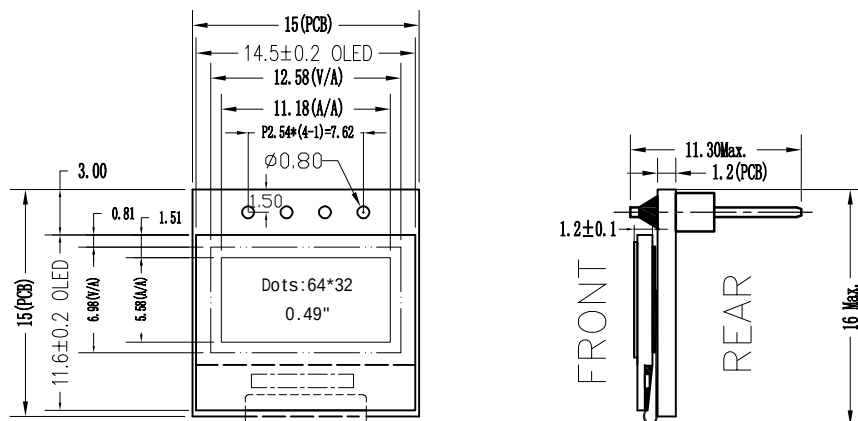
B

C

D

VER.	REVISED RECORD	DATA
V1.0	第一次发行(PCB VER1.0)	2020-10-18

1. 外形图



2. 功能&特性

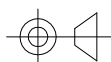
- 2-1. 点阵: 64X32 Dots
- 2-2. 模式: 正性
- 2-3. 单色显示
- 2-4. 接口方式: IIC
- 2-5. 模块供电电源(模块第2脚VCC): 3.3V~5V
- 2-6. LCD驱动电压V_{CC}(VOP): 7.0~7.5V(Generated by Internal DC/DC)
- 2-7. 工作温度: -40℃~85℃
- 2-8. 储存温度: -40℃~85℃
- 2-9. 连接方式/驱动IC: COG/SSD1315

14P间距0.7mm FPC焊接

3. 机械规格

- 3-1. 模块尺寸: 15mm(L)*16mm(W)*11.3 Maxmm(T)
- 3-2. 可视区域(V/A): 12.58mm(L)*6.98mm(W)
- 3-3. 有效区域(A/A): 11.18mm(L)*5.58mm(W)
- 3-4. 点距离: 0.175mm(L)*0.175mm(W)
- 3-5. 点尺寸: 0.155mm(L)*0.155mm(W)

NO.	Symbol
1	GND
2	VCC
3	SCK
4	SDA



SHEET: 1 of 1

APPROVALS

DATE

DWN

ZJP

2020-10-18

CHK

APP

金逸晨电子有限公司

MODEL NUMBER :
GME6432-06

SCALE:

Unspecified TOL:± 0.1

UNITS:

MM

DO NOT SCALE THIS DRAWING.

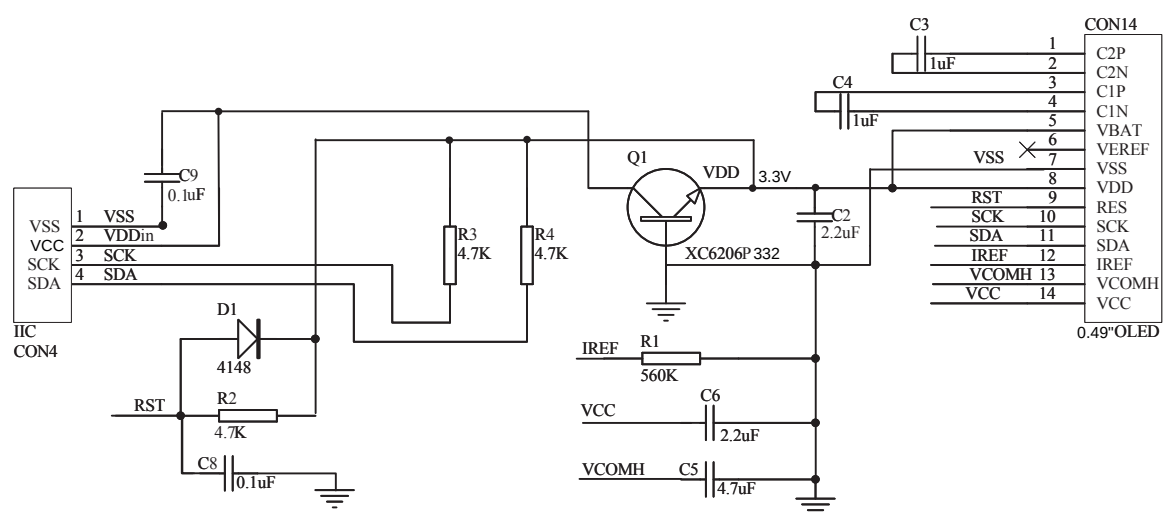
A

B

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D

4. 原理图:



5. 引脚说明:

Pin no.	Symbol	Function
1	GND	地
2	VCC	电源输入供电电压(3.3~5V)
3	SCK	时钟
4	SDA	数据

6. 电气特性

6-1、DC Characteristics

Characteristics	Symbol	Conditions	Min	Typ	Max	Unit
Supply Voltage for Logic	V_{DD}		1.65	2.8	3.3	V
Supply Voltage for Display (Supplied Externally)	V_{CC}	Note 5 (Internal DC/DC Disable)	7	-	9.0	V
Supply Voltage for DC/DC	V_{BAT}	Internal DC/DC Enable	3.5	-	4.2	V
Supply Voltage for Display (Generated by Internal DC/DC)	V_{CC}	Note 6 (Internal DC/DC Enable)	7	7.25	7.5	V
High Level Input	V_{IH}	$I_{OUT} = 100\mu A, 3.3MHz$	$0.8 \times V_{DD}$	-	V_{DD}	V
Low Level Input	V_{IL}	$I_{OUT} = 100\mu A, 3.3MHz$	0	-	$0.2 \times V_{DD}$	V
High Level Output	V_{OH}	$I_{OUT} = 100\mu A, 3.3MHz$	$0.9 \times V_{DD}$	-	V_{DD}	V
Low Level Output	V_{OL}	$I_{OUT} = 100\mu A, 3.3MHz$	0	-	$0.1 \times V_{DD}$	V
Operating Current for V_{DD}	I_{DD}		-	180	300	μA
Operating Current for V_{CC} (V_{CC} Supplied Externally)	I_{CC}	Note 7	-	7	13	mA
Operating Current for V_{BAT} (V_{CC} Generated by Internal DC/DC)	I_{BAT}	Note 8	-	12	17	mA
Sleep Mode Current for V_{DD}	$I_{DD, SLEEP}$		-	1	5	μA
Sleep Mode Current for V_{CC}	$I_{CC, SLEEP}$		-	2	10	μA

Note 5 & 6: Brightness (L_{br}) and Supply Voltage for Display (V_{CC}) are subject to the change of the panel characteristics and the customer's request.

Note 7: $V_{DD} = 2.8V, V_{CC} = 7.25V, 100\%$ Display Area Turn on.

Note 8: $V_{DD} = 2.8V, V_{CC} = 7.25V, 100\%$ Display Area Turn on.

* Software configuration follows Section 4.4 Initialization.

6-2、AC电气特性

I²C Interface Timing Characteristics:

Symbol	Description	Min	Max	Unit
t_{cycle}	Clock Cycle Time	2.5	-	μs
t_{HSTART}	Start Condition Hold Time	0.6	-	μs
t_{HD}	Data Hold Time (for "SDA _{OUT} " Pin)	0	-	ns
	Data Hold Time (for "SDA _{IN} " Pin)	300		
t_{SD}	Data Setup Time	100	-	ns
t_{SSTART}	Start Condition Setup Time (Only relevant for a repeated Start condition)	0.6	-	μs
t_{SSTOP}	Stop Condition Setup Time	0.6	-	μs
t_{R}	Rise Time for Data and Clock Pin		300	ns
t_{F}	Fall Time for Data and Clock Pin		300	ns
t_{IDLE}	Idle Time before a New Transmission can Start	1.3	-	μs

* ($V_{\text{DD}} - V_{\text{SS}} = 1.65\text{V to } 3.3\text{V}$, $T_{\text{a}} = 25^{\circ}\text{C}$)